LTP8803-1A Data Sheet Revision

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Table of Changes – 1

Datasheet Section	Parameter	Old	New
Pin Description	SYNC	Synchronization Input Signal. This pin is used as a reference for the internal oscillator and is referenced to GND. Synchronization is disabled by default. To enable synchronization, set 0xFE55[6] = 0 and then set 0xFE00 = 0b0100000 for the value to take effect. It is recommended that this input be disabled when not in use. To disable, set 0xFE55[6] = 1 and then set 0xFE00 = 0b0100000 for the value to take effect. To accomplish phase interleaving of multiple devices, a phase delay in steps of 22.5 degrees can be added using register 0x37[3:0].	Synchronization Input Signal. This pin is used as a reference for the internal oscillator and is referenced to GND. The synchronization is disabled by default. Synchronization is configured through register 0xFE55. After changes are made to 0xFE55, the user must then set 0xFE00[6] = 1 for the change to take effect. To enable synchronization, set 0xFE55[6] = 0 and then set 0xFE00[6] = 1 for the value to take effect. For phase interleaving, an optional phase delay in steps of 22.5 degrees can be added using register 0x37[3:0]. If the SYNC pin is not used, it should be either directly connected to GND or connected through a resistor (≤10kΩ). In either case, set bit 0xFE55[6] = 1, then set 0xFE00[6] = 1 for the value to take effect. When using a resistor, whether the SYNC function is enabled or not, ensure that the SYNC pin signal consistently meets its specifications in the Electrical Characteristics table.

Table of Changes - 2

Datasheet Section	Parameter	Old	New
Electrical Specifications	$I_{MON(OUT)}$	±3% *typical* I_{OUT} = 160A 0°C ≤ T_J ≤ 125°C	(no equivalent)
		(no equivalent)	±5% *guaranteed* 80A ≤ I_{OUT} ≤ 160A T_J = 25°C
		(no equivalent)	±5% *typical* 80A ≤ I_{OUT} ≤ 160A T_J = 0°C to 125°C
		(no equivalent)	±3.6A *guaranteed* 0A ≤ I_{OUT} ≤ 80A T_J = 25°C
		(no equivalent)	±5A *typical* 0A ≤ I_{OUT} ≤ 80A T_J = 0°C to 125°C
Revision History	Revision Code (REV)	B	C

AHEAD OF WHAT'S POSSIBLE

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